

A report on a seminar on

Embedded Systems Expert Talk

Date : 11-Aug-2025

Time : 11:00 AM - 01:00 PM

Organized by

Government Polytechnic Jamnagar

Under the initiative of

Student Startup & Innovation Policy (SSIP) Grant

Event Details

Venue : Computer lab, EC Department, New Building, G P Jamnagar

Timing : 11:00 AM - 01:00 PM (11-Aug-2025)

Registration Link : NA (ALL EC Students Present)

Organizing Committee Members :

Shri P. H. Kanani (Principal GP Jamnagar)

Shri R. K. Borsaniya (SSIP Co-ordinator)

Shri A. M. Bhatt (Event Coordinator)

Event Speaker :

Shri Karan Mau, Embedded Systems Expert, Ariton

Participation : 31 Students

11-08-2025

An expert talk on **Embedded Systems** for Innovative SSIP & other projects was conducted on **11th August 2025**, from **11:00 AM to 01:00 PM**, at the **Computer Lab, EC Department, New Building, Government Polytechnic, Jamnagar**. The session was specially arranged for **Electronics & Communication (EC) Department students**, with **31 students** in attendance.

The event was organized under the guidance of **Shri P. H. Kanani, Principal, Government Polytechnic, Jamnagar**, with coordination by **Shri R. K. Borsaniya, SSIP Coordinator** and **Shri A. M. Bhatt, Event Coordinator**.

The invited speaker, **Shri Karan Mau, Embedded Systems Expert, Ariton**, addressed the students on the role of **embedded systems in developing practical electronic solutions**. The session highlighted how microcontrollers, sensors and software can work together to perform monitoring, control and automation tasks.

The discussion helped students understand the relevance of embedded systems to **innovative projects under SSIP**. It emphasized connecting classroom knowledge with practical development, selecting suitable components, integrating hardware and software, and testing prototypes systematically. Students were encouraged to identify real-world problems and explore embedded technologies to develop useful solutions.

The session concluded with **concluding remarks and a vote of thanks by Shri A. M. Bhatt sir**, EC Department I/C Head. He appreciated the speaker and organizing committee and encouraged students to apply the knowledge gained to their academic and SSIP projects. He emphasized hands-on learning, teamwork and continuous experimentation as essential steps toward transforming ideas into working prototypes.

Few Event Photos



Event Brochure



A Seminar on



Education Department,
Government of Gujarat

An introduction to the world of **EMBEDDED SYSTEMS EXPERT TALK**

EVENT ORGANIZING COMMITTEE

Patron

Shri. P. H. Kanani

Principal
G P Jamnagar

Convener

Shri. R. K. Borsaniya

SSIP Coordinator
G P Jamnagar

Coordinator

Shri. A. M. Bhatt

I/C Head, EC Department
G P Jamnagar

EVENT SPEAKER

Shri. Karan Mau

Embedded Systems Engineer
Ariton

DATE AND PLACE

**THURSDAY, 11 AUG 2025
FROM 11:00 AM TO 01:00 PM
COMPUTER LAB, EC DEPARTMENT,
NEW BUILDING, G P JAMNAGAR**

CONTACT FOR REGISTER

✉ gpjssip@gmail.com

🌐 www.gpjamnagar.ac.in

~End of Report~